



Department of
Electronics & Communication
Engineering

JNTUA College of Engineering,
Anantapuramu (Autonomous)

Experts



Dr. Veeresh Babu V
Manager
AMS Semiconductors



Veda Prakash Mangu
HCL- Technologies

is Organizing
two day online workshop on

**VLSI CIRCUITS
ANALOG & DIGITAL
DESIGN
PERSPECTIVES**

(16TH & 17TH MAY 2020)

in Association with

**AMS Semiconductors
&
HCL Technologies**



ABOUT JNTUACEA

JNTUA College of Engineering, Anantapur stands aloft on a solid foundation of past glory and prestige. This college was started in the year 1946, under the post war reconstruction program and functioned at Guindy campus, Madras for two years and was shifted to Anantapur in 1948. It was in the year 1958 that the college was shifted to the present buildings. It has since 1973 been a constituent college of Jawaharlal Nehru Technological University, as set by The Jawaharlal Nehru Technological University Act, 1972. In 2008 it had received autonomous status by the Jawaharlal Nehru Technological Universities Act, 2008.

ABOUT DEPARTMENT

The department of Electronics and Communication Engineering is established in the year 1974. The department has come a long way since its inception, keeping pace with the changing needs and expectations of the society. The department takes pride in its highly placed alumni all over the globe. At present, the department is running a full time UG program in Electronics and Communication Engineering, three full time PG programs in Digital Electronics & Communication Systems (DECS), VLSI System Design (VLSISD) and Internet of Things IOT (Industry collaboration) specializations besides Ph.D. degree program in emerging areas of relevant technology.

OUR VISION

To produce globally and socially sensitized engineering graduates and to bring out quality research in the frontier areas of Electronics & Communication Engineering.

OUR MISSION

To provide quality and contemporary education in the domain of Electronics & Communication Engineering through periodically updated curriculum, best of breed laboratory facilities, collaboration with industries and effective teaching learning process. To provide essential knowledge on research and innovative technologies in Electronic & Communication Engineering and to inculcate innovative skills, research aptitude, teamwork, ethical practices in students so as to meet expectations of the industry as well as society.

TARGET AUDIENCE:

All UG/ PG /faculty of ECE & EEE

WORKSHOP OBJECTIVE

As all people are staying home due to coronavirus (covid-19) Pandemic. This online course provides an introduction on designing of analog & digital VLSI circuits through Economics, Architecture, Layout, Validation, testing, also address on ASIC & SOC design.

WORKSHOP SCHEDULE

Day 1 (10:30am -12:30pm)

- Motivation
- Economics of IC Design
- IC Architecture Concept and Planning
- Design for What?
- Understanding system/application requirements

Day 1 (2:30pm -4:30pm)

- Digital IC Design
- ASIC Design methodology
- IP Design

Day 2 (10:30am -12:30pm)

- Overview of SOC Design
- Front end & back end basics
- A big picture of an integrated circuit

Day 2 (2:30pm -4:30pm)

- What happens after layout? (GDS to IC)
- Validation
- ATE testing
- Qualification & "Unlimited" production
- Closing ceremony

CERTIFICATION

E- Certificate will be Provided

FREE REGISTRATION

REGISTRATION LINK

<https://forms.gle/i14HsZC5ovM5Jxnf6>

COORDINATOR

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